

PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of: Kwang-Rae CHO

Serial No.: New Application Group Art Unit: Not Yet Assigned

Filed: July 11, 2003 Examiner: Not Yet Assigned

Title: SEMICONDUCTOR DEVICE WITH CLOCK ENABLE BUFFER TO PRODUCE  
STABLE INTERNAL CLOCK SIGNAL

**INFORMATION DISCLOSURE STATEMENT**

Honorable Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

July 11, 2003

Sir:

As a means of complying with the duty of disclosure under 37 CFR §1.56, and in accordance with 37 CFR §§1.97 and 1.98, Applicant(s), through the undersigned attorney, submits this Information Disclosure Statement. The patents, publications or other information submitted herewith are listed on the attached Form PTO-1449 and copies are attached.

In accordance with 37 CFR §1.97(b) (1) or (2), this Information Disclosure Statement is being filed either within three months of the filing date of the above-identified application, or within three months of the date of entry into the national stage of the above-identified application as set forth in 37 CFR §1.491. Accordingly, no fee is required.

Respectfully submitted,

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## LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT

ATTY. DOCKET NO.: P68982US0 GROUP ART UNIT: Not Yet Assigned  
 SERIAL NO.: New Application FILING DATE: July 11, 2003  
 APPLICANT(S): Kwang-Rae CHO TODAY'S DATE: July 11, 2003

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## U.S. PATENT DOCUMENTS

*EXAMINER <u>INITIAL</u>	DOCUMENT NUMBER	DATE	NAME	INT'L CLASS	SUB- CLASS	FILING DATE (If Appropriate)
AA	<u>5,880,998</u>	<u>03/09/99</u>	<u>Tanimura et al.</u>	<u>G11C</u>	<u>7/00</u>	<u>10/29/97</u>
AB	<u>6,359,487</u>	<u>03/19/02</u>	<u>Heightley et al.</u>	<u>H03H</u>	<u>11/26</u>	<u>04/03/00</u>

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## FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION (YES) (NO)
AC	<u>2-244656</u>	<u>09/28/90</u>	<u>Japan</u>	<u>H01L</u>	<u>21/82</u>	<u>X</u>
AD	<u>2001-44283</u>	<u>02/16/01</u>	<u>Japan</u>	<u>H01L</u>	<u>21/82</u>	<u>X</u>
AE	<u>2001-222887</u>	<u>08/17/01</u>	<u>Japan</u>	<u>G11C</u>	<u>11/407</u>	<u>X</u>
AF	<u>2002-367391</u>	<u>12/20/02</u>	<u>Japan</u>	<u>G11C</u>	<u>19/28</u>	<u>X</u>
AG	<u>11-15783</u>	<u>01/22/99</u>	<u>Japan</u>	<u>G06F</u>	<u>13/42</u>	<u>X</u>
AH	<u>9-198875</u>	<u>07/31/97</u>	<u>Japan</u>	<u>G11C</u>	<u>11/413</u>	<u>X</u>

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## OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

AI \_\_\_\_\_  
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EXAMINER \_\_\_\_\_ DATE CONSIDERED \_\_\_\_\_

\* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).

# **Information Disclosure Statement**

New U.S. Patent Application for  
SEMICONDUCTOR DEVICE WITH CLOCK ENABLE BUFFER TO  
PRODUCE STABLE INTERNAL CLOCK SIGNAL  
Our Ref. No.: P02H6023/US/iw

Reference No.:

- (1) US Patent No. 5,880,998
- (2) US Patent No. 6,359,487
- (3) JP Laid-Open No. 02-244656
- (4) JP Laid-Open No. 2001-44283
- (5) JP Laid-Open No. 2001-222887
- (6) JP Laid-Open No. 2002-367391
- (7) JP Laid-Open No. 11-15783
- (8) JP Laid-Open No. 09-198875